

ABSTRACT

Imaging circuits and associated methods implementing time-domain low-pass filter functionality on an imaging chip. An imager chip is disclosed, wherein the chip includes pixel and memory arrays, a data subtraction circuit, and image enhancement circuit and an analog-to-digital converter. Under an alternate embodiment, an averaging circuit is coupled between the pixel array and memory array. A unique pixel circuit and memory circuit are further disclosed for an imager system, wherein incoming signals are sampled and processed to remove noise components from the signal.